

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7	(blackmore).in. and (international adj business).as.	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 20:28
S1	2	"20050080920"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 09:07
S2	149	(communicat\$5 near3 adapter\$3) and (((slave\$3 second\$4 backup (back-up)) and (master\$3 primary\$3)) adj3 adapter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 09:14
S3	138	(transfer\$5 transmit\$5 send\$5) near9 (packet\$5) with (communicat\$5 near2 adapter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 09:20
S4	79	S3 and @ad<"20031014"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 09:25
S5	4	S3 and @ad<"20031014" and ("without" near4 stor\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 09:18
S6	13	((transfer\$5 transmit\$5 send\$5) near9 (packet\$5) with direct\$4 near5 ((target\$3 destination\$3 receiver\$3 recipient\$3) near4 (memor\$5 buffer\$3))) and (communicat\$5 near2 adapter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 09:23
S7	1	S6 and @ad<"20031014"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 11:59

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S8	5	"6665278"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 09:42
S9	53	"6246683"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 09:42
S10	30	S9 and @ad<"20031014" and adapter\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 12:15
S11	25	S10 and ((target\$5 destinat\$5 receiver\$5 recipient\$5) near9 (memor\$5 buffer\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 11:00
S12	30	"6074777"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 10:32
S14	749	(source adj3 (buffer\$5 memor\$5)) with ((target\$3 destinat\$5) adj3 (buffer\$3 memor\$5)) with (transfer\$5 transmit\$4 send\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 12:23
S15	0	S13 and @ad<"20031014" and adapter\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 12:00
S16	58	S14 and @ad<"20031014" and adapter\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 12:14
S17	8	("without" with intermediat\$5 with cop\$4) with (nic interface\$3 adapt\$4) with (source\$3 destination\$3 target\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 12:18

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S18	3	S17 and @ad<"20031014"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 12:18
S19	36	((("without" with intermediat\$5 with cop\$4) with (nic interface\$3 adapt\$4)) and (source\$3 destination\$3 target\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 12:18
S20	21	S19 and @ad<"20031014"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 12:24
S21	196	(source adj3 (buffer\$5 memor\$5)) with ((target\$3 destinat\$5) adj3 (buffer\$3 memor\$5)) with (dma (direct adj memor\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 12:24
S22	22	S21 and @ad<"20031014" and (nic adapter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 12:31
S23	6	("5828903" "6122759" "5778180"). pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/27 12:30



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» Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. Using the Memory Channel Network
Gillett, R.; Kaufmann, R.;
[Micro, IEEE](#)
Volume 17, [Issue 1](#), Jan.-Feb. 1997 Page(s):19 - 25
Digital Object Identifier 10.1109/40.566189
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Using the Memory Channel Network

As shown in Figure 4, an I/O device can **transmit directly** to a **Memory** Channel adapter.

... They write messages directly into the **target** buffers—there are no ...

doi.ieeecomputersociety.org/10.1109/40.566189 - [Similar pages](#)

ATM/SONET network enhanced as a universal computer system ...

For example, a CPU may perform a communications **transmit directly** to CPU cache, a

cache may perform a disk read directly to cache, and a **memory** agent may ...

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Wireless networked message routing - US Patent 6665278

Initially, the originating node ON makes an unsuccessful attempt to **transmit directly** to the **target** node TN. After incrementing the hop count to 1, ...

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[PDF] Cognitive schema theory in the constructivist debate.

Although it is possible to **transmit directly** to students the facts and ideas of a **target** **memory** objects—certain statistical and critical-think- ...

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RE: iSCSI: RE: Framing Discussion

But that would >> require the NIC to implement a BWDP's worth of transmit buffer **memory**.

>> >> To have the iSCSI TOE re-transmit **directly** from the buffer ...

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tem overhead for the signaling mechanism. As shown in Figure 4, an I/O device can

transmit directly. to a **Memory** Channel adapter. This allows, for example, ...

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Secure communication system - Patent 5671285

c.3) means responsive to said local control means, to determine whether to **transmit directly** to the selected **target** terminal unit. ...

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Using direct cluster member to cluster member links to improve ...

2, member CM2 communicates with a **target** co-cluster member CM3 by routing 4, CM2 may decide to **transmit directly** to CM3, instead of routing messages ...

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[PDF] 1 Overview

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The following **target** hardware and tools are required to build and execute D from the Am186CC microcontroller must be able to **transmit directly** to the ...

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